Searc	ch No	otes	

	Application/Control No.	Applicant(s)/Patent under Reexamination
	10/767,001	ZHANG ET AL.
	Examiner	Art Unit
	Christophor E. Loo	2111

	SEARCHED		
Class	Subclass	Date	Examiner
710	113 305	12/8/2005	CEL
711	122 133	12/8/2005	CEL
711	154	12/8/2005	CEL
370	452	12/8/2005	CEL
Above	UPDATED	3/13/2006	CEL
Above	UPDATED	10/28/2006	CEL
Above	UPDATED	3/14/2007	CEL

INTERFERENCE SEARCHED			
Class	Subclass	Date	Examiner
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SEARCH NOT (INCLUDING SEARCH)
	DATE	EXMR
EAST - Data Transferring Control with Buffering scheme, Caching latency reducing technique	12/8/2005	CEL
Search Updated .	3/13/2006	CEL
Case Reopen after Appeal	10/26/2006	CEL
EAST [USPT, JPO, EPO, IBM_TDB] Buffer Bypassing controlfor reducing data transfer latency See Search History printout	10/28/2006	CEL
Search Updated	3/14/2007	CEL
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